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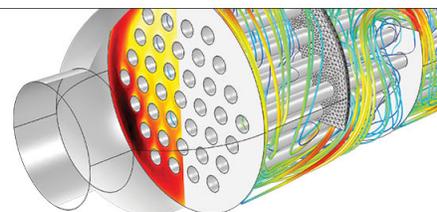
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Highly *c*-axis oriented Pb(Zr, Ti)O₃ thin films grown on Ir electrode barrier and their electrical properties

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We have investigated the structural and electrical properties of sol-gel derived Pb(Zr, Ti)O₃ (PZT) thin films deposited on Ir electrode barrier (Ir/poly-Si/SiO₂/Si). Owing to the interface-controlled growth, highly *c*-axis oriented perovskite PZT thin films were obtained for the postdeposition annealing temperature of 580 °C. Additionally, we found that the ferroelectric properties of IrO₂/PZT/Ir/poly-Si capacitors were remarkably changed by the partial pressure of oxygen during the deposition of IrO₂ top electrodes, which could be due to the enhanced reaction of IrO₂ with PZT by the oxygen ion bombardments. Remanent polarization and coercive field of IrO₂/PZT/Ir/poly-Si capacitor with the top electrodes deposited at $P_{O_2} = 1$ mTorr was 20 $\mu\text{C}/\text{cm}^2$ and 30 kV/cm, respectively, and showed negligible polarization fatigue up to 10¹¹ switching repetitions. The leakage current density at a field of 80 kV was 5×10^{-8} A/cm². © 1999 American Institute of Physics. [S0003-6951(99)01510-7]

The development of high-density nonvolatile ferroelectric memories (NvFRAMs) is one of the interesting subjects in fields of memory devices. In a one transistor-one capacitor (1T-1C) NvFRAM cell structure,¹ the bottom electrode of the capacitor is directly contacted with poly-Si plug, which in turn is connected to the source/drain of the transistor in the memory cell. Hence, the bottom electrode is required to act as an oxygen diffusion barrier in order to prevent the reaction of oxygen with poly-Si plug at highly oxidizing ambient during the fabrication of ferroelectric film. In addition, Pb(Zr, Ti)O₃ (PZT) based high-density NvFRAMs require the bottom electrode of the capacitor to help in eliminating the degradation properties, such as polarization fatigue, aging, and retention. Recently, Ir and IrO₂ have attracted attention as electrode materials for PZT thin film capacitors since they showed high quality ferroelectric properties which are comparable to those of Pt/PZT/Pt capacitors. However, most reported PZT capacitors with such electrodes have hybrid electrode structures,²⁻⁴ such as Pt/IrO₂ and Ir/IrO₂. Since they have more complex processes and relatively large thickness, such electrode structures may be unsuitable for the integration of NvFRAMs. Moreover, if one would try to use such hybrid electrodes as a bottom electrode in the 1T-1C cell structure, the additional layer⁵ for oxygen barrier should be needed in order to protect the reaction of oxygen with poly-Si. Hence, the development of a simple and thin electrode-barrier structure is necessary from the practical aspect point of view. Recently, we have successfully prepared the high-quality PZT capacitor having the most simple structure, IrO₂/PZT/Ir/poly-Si, which showed that Ir in itself could act as an electrode barrier. In this letter, we present the microstructures and electrical properties of PZT thin films deposited onto Ir/poly-Si substrates. An attempt is made to realize the origin of orientation of PZT thin films during the

growth. As a result, we could successfully prepare highly *c*-axis oriented perovskite rich PZT thin films. In order to characterize the electrical properties of PZT thin film capacitor, we also investigated the effect of top electrodes on their electrical properties.

Ir layer of 120 nm thickness was deposited onto *n*⁺ poly-Si/SiO₂/Si wafers using a radio-frequency (rf) magnetron sputtering process, which is described elsewhere.⁵ PZT thin films of about 250 nm thickness, with a composition of Pb_{1.1}(Zr_{0.53}Ti_{0.47})O₃, were deposited on Ir/poly-Si by means of a conventional sol-gel method, which is described elsewhere.⁶

Figure 1 shows the x-ray diffraction (XRD) patterns of PZT/Ir/poly-Si thin films annealed at different temperatures. It could be seen that all PZT thin films annealed at temperatures greater than 560 °C had perovskite single phase with

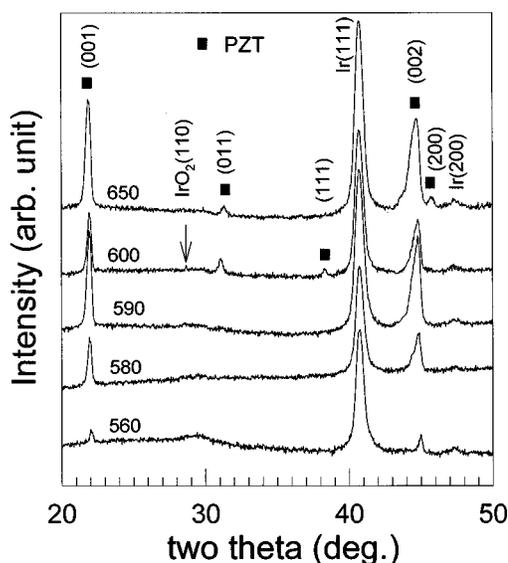


FIG. 1. X-ray diffraction patterns of PZT/Ir/poly-Si thin films postannealed at different temperatures ranging from 560 to 650 °C.

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highly *c*-axis orientation. However, for annealing temperatures greater than 600 °C, PZT (011) and (111) peaks appeared in XRD patterns of Fig. 1, which were accompanied with small IrO₂ (110) peak at $2\theta=28.4^\circ$. Such IrO₂ peaks could be accompanied by the formation of an amorphous Pb–Ir–O phase formed due to the reaction of Ir with PbO in PZT during annealing process. Work done earlier by our group⁷ had shown that PZT thin films processed on as-deposited IrO₂/Ir electrodes possessed random orientation. Additionally, it was also shown that PZT thin films processed on annealed IrO₂/Ir electrodes a possessed a greater tendency of *c*-axis orientation. While our sol-gel derived PZT thin films deposited on a Pt/Ti/SiO₂/Si substrate exhibited highly (111) preferred orientation,⁶ those deposited on Ir/poly-Si substrates showed highly *c*-axis orientation. The fact that the orientation of PZT thin films is related to the materials of bottom electrodes implies that the crystallization of PZT thin films on metal electrodes is mainly dominated by the interface-controlled growth. The orientation of PZT thin films on Pt bottom electrode, as reported by many workers, depends strongly on the heating treatments as well as on the deposition methods. Among them, Chen and Chen⁸ suggested the interface growth in PZT, that is, when a (111)-oriented Pt_{5.7}Pb intermetallic phase was formed during the pyrolysis step, it provided the template for the growth of (111)-oriented PZT, while if (001) PbO formed first a (001) PZT orientation was observed since there is a reasonable lattice match between (001) PbO and (001) PZT. In the case of PZT/Ir/poly-Si, amorphous Pb–Ir–O phase was the most possible interfacial phase between PZT and Ir bottom electrodes.⁹ Hence, we assumed that highly *c*-axis oriented PZT on Ir might be due to the preferential growth so as to minimize surface energy, since a (001) texture represented minimum surface energy. On the other hand, if once (111) or (110) IrO₂ phases were formed, growth of PZT might be interfered by solid-phase epitaxial effect resulting in randomly oriented PZT films. However, further investigations would be needed in order to confirm our assumption.

We investigated the fundamental electrical properties, such as *P*–*E* hysteresis loop, polarization fatigue behavior, and the leakage current–voltage (*I*–*V*) characteristics, of highly *c*-axis oriented PZT thin film capacitors annealed at 580 °C. The details of the electrical measurement are described elsewhere.^{5,7} All the electrical measurements were performed by using a two probe contact between the surface of poly-Si and IrO_x top electrode. The IrO_x top electrodes of 3.5×10^{-4} cm² area and 80 nm thickness were deposited using the rf-reactive sputtering of Ir target in Ar+O₂ ambience. The partial pressure of Ar (*P*_{Ar}) was fixed at 2 mTorr, but that of O₂ (*P*_{O₂}) was varied from zero to 3 mTorr in order to investigate the optimum deposition conditions of IrO_x top electrode. Figure 2 shows the typical *P*–*E* hysteresis loops of IrO_x/PZT/Ir/poly-Si capacitors with different *P*_{O₂}. It can be seen that as *P*_{O₂} is increased, the symmetry of the *P*–*E* hysteresis loop has increased, which is also shown in the inset of Fig. 2 as a plot of median between two coercive field (+*E*_c and –*E*_c) vs *P*_{O₂}. In order to interpret these results, we investigated the compositional ratio of Ir/O in IrO_x with different *P*_{O₂} using electron spectroscopy for chemical analy-

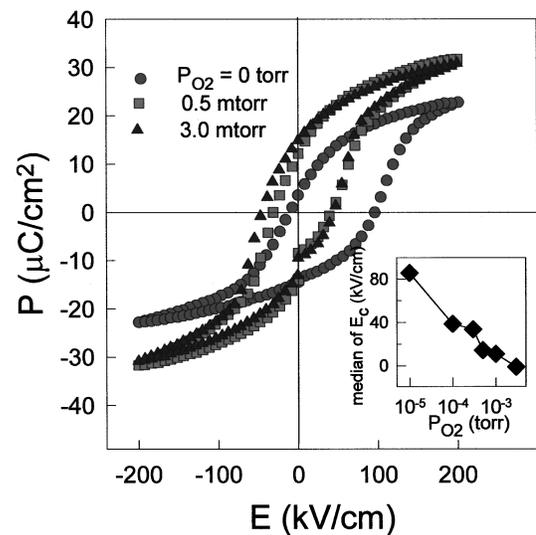


FIG. 2. Typical *P*–*E* hysteresis loops of IrO₂/PZT/Ir/poly-Si capacitors, where IrO₂ top electrode were sputter deposited at different oxygen partial pressures, *P*_{O₂}. The inset of this figure shows the plot of median of *E*_c [= (+*E*_c) + (–*E*_c)] vs *P*_{O₂}.

sis (ESCA). However, for all IrO_x films deposited with different *P*_{O₂}, the atomic ratios of Ir/O were absolutely the same as 32/68, which corresponded to IrO₂. Hence, we thought that the results in Fig. 2 might be due to the oxygen ion bombardments during the deposition of top electrodes rather than due to the oxygen contents in IrO₂ top electrodes. As *P*_{O₂} was increased, the oxygen ion bombardments with the PZT/Ir/poly-Si substrates were increased, which enhanced the reaction of top IrO₂ with PZT. The interfacial states between top IrO₂ electrodes and PZT need to be the same as those between PZT and bottom Ir electrodes in order to obtain a symmetrical *P*–*E* hysteresis loop. *P*_r was about 20 μC/cm² at applied voltages of 5 V (200 kV/cm) and *E*_c was about 40 kV/cm for PZT capacitors with the top electrode deposited at *P*_{O₂} greater than 0.5 mTorr. Figure 3 shows the typical polarization–fatigue behaviors of PZT capacitors with different top electrodes. In Fig. 3, the normalized difference between the switched and nonswitched polar-

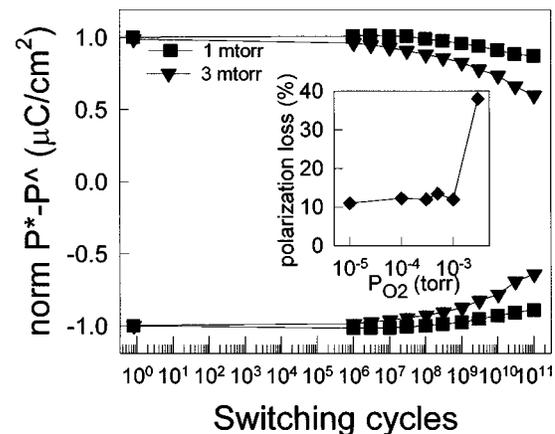


FIG. 3. Typical polarization fatigue behaviors of IrO₂/PZT/Ir/poly-Si capacitors having different IrO₂ top electrode deposited at *P*_{O₂}. The inset of these figure shows the plot of polarization loss after 1×10^{11} switching repetitions vs *P*_{O₂}.

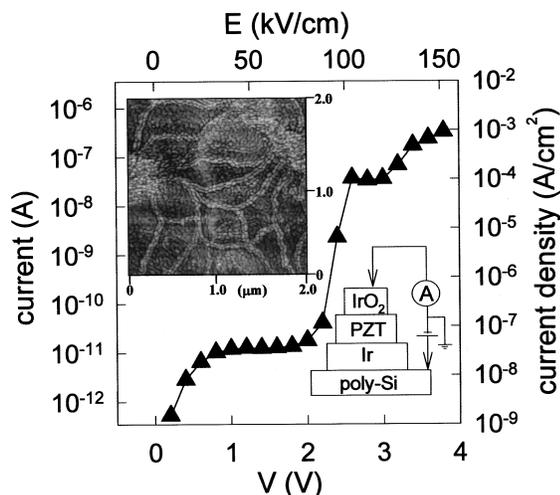


FIG. 4. Leakage current-voltage characteristic of $\text{IrO}_2/\text{PZT}/\text{Ir}/\text{poly-Si}$ capacitor having IrO_2 top electrode deposited at $P_{\text{O}_2} = 1$ mTorr. The inset of this figure shows an atomic force microscopy image ($2 \times 2 \mu\text{m}^2$) of the surface of $\text{PZT}/\text{Ir}/\text{poly-Si}$ capacitor.

ization ($P^* - P^{\wedge}$) was plotted as a function of switching cycles applied to the capacitors. It could be observed in the inset of Fig. 3 that all capacitors with IrO_2 top electrodes deposited at P_{O_2} less than 1 mTorr had negligible fatigue loss of around 10% after 1×10^{11} polarization repetitions. However, PZT capacitor IrO_2 top electrodes deposited at $P_{\text{O}_2} = 3$ mTorr exhibited about 40% polarization loss. Such results indicated that excessive reaction of the top electrode with PZT by oxygen ion bombardments could be a cause of defects which might be the origin of polarization fatigue in PZT capacitors. Hence, a well controlled top electrode deposition might enhance the fatigue resistivity of PZT capacitors. Figure 4 shows the $I-V$ characteristics of $\text{IrO}_2/\text{PZT}/\text{Ir}/\text{poly-Si}$ capacitors with the top electrodes deposited at $P_{\text{O}_2} = 1$ mTorr. In Fig. 4, it could be seen that for fields smaller than 80 kV/cm, the leakage current densities were below $1 \times 10^{-7} \text{ A/cm}^2$. However, above intermediate fields of 80 kV/cm, a high leakage current of around $1 \times 10^{-4} \text{ A/cm}^2$ could be seen, which was normally observed for PZT capacitors with high defect concentration, such as for PZT capacitors with conducting oxide electrodes. This suggests that in the $\text{IrO}_2/\text{PZT}/\text{Ir}/\text{poly-Si}$ capacitors, the undetected interfacial phase formed at the interface between

PZT film and both electrodes might have diminished the microstructure of PZT layer. Hence, there could have been enough defect states in PZT layer which caused the PZT capacitors to be leaky. Such a fact could be confirmed from atomic force microscopic images ($2 \times 2 \mu\text{m}^2$) of the $\text{PZT}/\text{Ir}/\text{poly-Si}$ surface, as shown in the inset of Fig. 4, which showed an appreciable amount of second phase nonuniformly distributed on the surface of PZT layer.

Conclusively, we have prepared highly c -axis oriented PZT thin films deposited on $\text{Ir}/\text{poly-Si}$. The orientation of these films were believed to be controlled by the preferential growth on the interfacial phase of amorphous Pb-Ir-O so as to minimize surface energy. The ferroelectric properties of $\text{IrO}_2/\text{PZT}/\text{Ir}/\text{poly-Si}$ capacitor were remarkably changed by the partial pressure of oxygen during the deposition of IrO_2 top electrodes, which was due to the enhanced reaction of IrO_2 with PZT by the oxygen ion bombardments. PZT capacitors with IrO_2 top electrodes deposited at $P_{\text{O}_2} = 1$ mTorr exhibited a highly symmetric $P-E$ hysteresis loop with remanent polarization and coercive field of $20 \mu\text{C/cm}^2$ and 30 kV/cm, respectively, and showed lack of polarization fatigue up to 10^{11} switching repetitions. The leakage current densities at a field of 80 and 120 kV were 5×10^{-4} and $1 \times 10^{-4} \text{ A/cm}^2$, respectively. The results of this work proved that Ir in itself could act as an bottom electrode barrier for PZT capacitor. If one could reduce the interfacial phases between PZT and electrodes, the leakage current behaviors would further improve and high quality $\text{IrO}_2/\text{PZT}/\text{Ir}/\text{poly-Si}$ capacitors with a simple electrode-barrier structure could be fabricated for high density NvFRAMs .

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