

## VITA

Shatil Haque was born in January 1, 1970 in Rajshahi, Bangladesh. He graduated in June 1985 from Government Laboratory High School, Dhaka, Bangladesh. He continued his study in Dhaka College and graduated in October 1987. Shatil Haque received his B.S. degree in Electrical Engineering in 1991 from Arizona State University and his M.S. degree in Electrical Engineering in 1993 from Virginia Polytechnic Institute and State University. While working at the NSF Center for Power Electronics Systems, he graduated in December 1999 with a Ph.D. degree from the Department of Materials Engineering and Science at Virginia Tech. Shatil Haque's research interest include materials development for electronic packaging, package design for high-power electronics modules, interface/adhesion engineering, processing of thick and thin film materials and materials characterization. He has worked as a teaching and research assistant for the Materials Science and Engineering Department and the Electrical and Computer Engineering Department for the past six years. He has taught courses related to electronic materials design, microelectronic fabrication, hybrid microelectronic materials design, semiconductor device physics, general materials science and electronic circuit design. His experience also includes building of a Power Electronics Packaging Laboratory for the Center for Power Electronics Systems and the Electrical and Optical Materials Characterization Laboratory for the Materials Science and Engineering Department. He is currently supervising the power electronics packaging laboratory, which is dedicated to the development and fabrication of power electronic packages. During his academic career, he has gained extensive work experience with screen printing equipment and furnaces for thick film deposition and processing, thin film deposition units, die-attach and wire-bonding equipment for hybrid microelectronic fabrication, photolithography facility for wet-etching, powder metallurgy equipment, polymer deposition and electrodeposition facilities for interface fabrication and thermal and electrical measurement equipment for characterization of electronic modules. Shatil Haque is a member of the IEEE-Components, Packaging and Manufacturing Technology and the International Microelectronics and Packaging Society.