

VITA
Sridhar Tirumala

Sridhar Tirumala was born in Hyderabad, Andhra Pradesh State in southern part of India. He was raised in Hyderabad, where, after receiving his high school degree in 1990, he was admitted to the Indian Institute of Technology-Madras for undergraduate studies. He completed his Bachelor of Technology in Metallurgical Engineering in 1990 from the same institute.

In 1995, he joined the Department of Materials Science and Engineering at Virginia Polytechnic Institute and State University. Graduate research was performed under the supervision of Prof. Seshu B. Desu, and this research in the area of ferroelectric thin films has currently turned into Ph. D. dissertation.

Sridhar Tirumala is currently working as a Senior Process Development Engineer at LSI Logic Inc.