

## Vita

Pulakesh Roy was born in Narayangonj, Bangladesh on December 20, 1972. He received his Bachelor of Science degree in Electrical and Electronic Engineering with highest Distinction from Bangladesh University of Engineering and Technology, Dhaka, Bangladesh in 1996. After that he worked as a Lecturer in the Department of Electrical and Electronic Engineering of Ahasanullah University of Engineering and Technology, Dhaka, Bangladesh till December 1997. He started as a Master's student in Electrical Engineering at Virginia Polytechnic Institute and State University in January 1998. Upon graduation he will be joining PCS Research Labs, Motorola in Harvard, IL.

His research interests are in the areas of blind adaptive algorithms, system identification, and low frequency audio processing.