

VITA

Qaisar Hameed was born on August 24, 1973 in Lahore, Pakistan. He got his B.Sc. degree in Electrical Engineering with an emphasis on Electronics and Communication from University of Engineering and Technology, Lahore. After completing his undergraduate studies, he worked in Advanced Engineering Research Organization, Haripur, Pakistan as Assistant Manager Electronics, for two years. He joined the Bradley Department of Electrical and Computer Engineering, Virginia Tech in August 1997 for graduate studies. There, he completed a Masters program in Electrical Engineering with an emphasis on Computer Engineering.

His research interests are design, modeling and testing of digital systems and VLSI circuit design. He plans to join the Logic Technology Development Group at Intel Corporation in Portland, Oregon as a Component Design Engineer in March 1999.