

# Designing a Software Defined Radio to Run on a Heterogeneous Processor

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Virginia Polytechnic Institute and State University  
in partial fulfillment of the requirements for the degree of

Master of Science  
in  
Electrical Engineering

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Date: Wed, 13 Apr 2011 18:17:13 -0700 [04/13/2011 09:17:13 PM EDT]  
From: Matt Ettus <matt@ettus.com>  
To: afayez@vt.edu  
Subject: Re: Permission to use USRP photos and diagrams for Master Thesis

On 04/13/2011 05:53 PM, afayez@vt.edu wrote:

Dear Matt, I would like to ask your permission to use photos, figures, and diagrams of USRPs for my Masters thesis. My thesis title is "Designing a Software Defined Radio to Run on a Heterogeneous Processor" and it essentially discusses the user and integration of the C64x+ DSP on the Beagleboard and porting the Virginia Tech CWT PSCR to run on the Beagleboard. Thanks.

Sincerely ,

Al Fayez

Sure, go ahead. Good luck on finishing, and please make sure to send me a copy.

Matt