

Vita

Phichet Trisiripisal was born in Bangkok, Thailand. He earned his B.E degree in Computer Engineering from Assumption University, Bangkok, Thailand in March 1999. He worked as lecturer assistant for Assumption University, doing research in digital signal processing before starting his Master of Science in Electrical Engineering at Virginia Polytechnic Institute and State University in Fall 2001. His research interest are in computer vision, signal and image processing, wavelet transform and triangulation.