

## **VITA**

Antoine Vidalinc

The author was born on July 11, 1973, and grew up in the North East of France. In June 1991, he obtained his French secondary school diploma with honors. Subsequently, he attended the university-level preparation in math and physics for the competitive entrance exams to French Grandes Ecoles. In October 93, he enrolled the Ecole Nationale Supérieure d'Ingenieurs Electriciens de Grenoble (leading French University in Electrical Engineering part of INPG: Grenoble National Polytechnic), graduating in 1996. In the summer 1996, the author registered in the graduate school at Virginia Polytechnic Institute and State University as a graduate research assistant, and received an M.S. degree in electrical engineering in the summer 1997.

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