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Description of item under review for fair use: Figure 2.6 A Typical Model of the FPGA Routing Problem. Jayaraman, Rajeev. "Physical design for FPGAs." In Proceedings of the 2001 international symposium on Physical design, pp. 214-221. ACM, 2001.

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Description of item under review for fair use: Figure 2.7 A Simplified FPGA Routing Graph. Betz, Vaughn, Jonathan Rose, and Alexander Marquardt. Architecture and CAD for deep-submicron FPGAs. Kluwer Academic Publishers, 1999.

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