

Dependence of Set, Reset and Breakdown Voltages of a MIM Resistive Memory Device on the Input Voltage Waveform

Gargi Ghosh

Thesis submitted to the faculty of the Virginia Polytechnic Institute and State University in
partial fulfillment of the requirements for the degree of

Master of Science
In
Electrical Engineering

Marius K. Orlowski, Chair

Louis J. Guido

Gino Manzo

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Blacksburg, VA

Keywords: resistive switch, non-volatile memory, conductive filament, threshold
distributions, interconnect, reliability study, TDDB



Gargi Ghosh <gargig89@vt.edu>

Regarding my MS thesis

3 messages

Gargi Ghosh <gargig89@vt.edu>
To: "King, Sean" <sean.king@intel.com>

Thu, Mar 26, 2015 at 7:05 PM

Dear Dr. King,

There is something I wished to discuss with you. As you already know, I am planning to write a chapter about on the results obtained in in the combined effort of SRC Project and Capstone Course. It is pretty going to be the same report I had sent you the last time with a few more additions.

As a background to the work, I was also planning to use a few of the images that were provided by you in the presentation for the Capstone course kick-off meeting. I would make necessary references and courtesies too.

However I do realize all the information belongs to Intel and hence I was seeking for your permission for the same. This project will be a huge addition in my thesis and your permission would mean a lot.

Thank you so much!

Sincerely,
Gargi.

—
Gargi Ghosh
Graduate Researcher
Bradley Department of Electrical and Computer Engineering
Virginia Polytechnic Institute and State University
gargig89@vt.edu | 540-808-8593

King, Sean <sean.king@intel.com>
To: Gargi Ghosh <gargig89@vt.edu>

Thu, Mar 26, 2015 at 7:12 PM

Hi Gargi,

I don't there is any issue with including in your thesis the images in the presentation I provided. All those images were approved for public consumption. As long as you make some acknowledgement to Intel or reference the original publication, you should be fine.

Thanks

Sean

From: Gargi Ghosh [mailto:gargig89@vt.edu]
Sent: Thursday, March 26, 2015 4:06 PM
To: King, Sean

Subject: Regarding my MS thesis

[Quoted text hidden]

Gargi Ghosh <gargig89@vt.edu>
To: "King, Sean" <sean.king@intel.com>

Thu, Mar 26, 2015 at 7:16 PM

Thank you so much, Dr. King. I will definitely make necessary acknowledgement and references.

Sincerely,
Gargi.

[Quoted text hidden]

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Description of item under review for fair use: Figure 1.23. Y. Kang, T. Liu, and M. K. Orlovski, "Pulse operation of a floating-electrode memristive device," IEEE Trans. Electron Devices, vol. 60, no. 4, pp. 1476-1479, Apr. 2013.

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