

Design Validation of RTL Circuits using Binary Particle Swarm Optimization and Symbolic Execution

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Thesis submitted to the Faculty of the
Virginia Polytechnic Institute and State University
in partial fulfillment of the requirements for the degree of

Master of Science
in
Computer Engineering

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June 19, 2015
Blacksburg, Virginia

Keywords: Design Verification; Particle Swarm Optimization; Static Analysis; Symbolic
Backward Execution; Satisfiability Modulo Theory; Pattern Search Methods

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IEEE PUBLICATION TITLE (Journal, Magazine, Conference, Book): **2015 IEEE Computer Society Annual Symposium on VLSI**

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