

A Framework for Validation and Testing of a CubeSat Retarding Potential Analyzer

Stephen E. Noel

Thesis submitted to the Faculty of the
Virginia Polytechnic Institute and State University
in partial fulfillment of the requirements for the degree of

Master of Science
in
Aerospace Engineering

Gregory D. Earle, Chair
Craig Woolsey
Kevin Shinpaugh

August 6, 2015
Blacksburg, Virginia

Keywords: Space Plasma, Ion Energy Spectrum, Calibration, Retarding Potential Analyzer,
Spacecraft

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Lucy Fanelli <lfanelli@vt.edu>
To: Stephen Noel <snoel07@vt.edu>

Wed, Aug 19, 2015 at 12:33 PM

Hey Stephen!

Yes you can use my figures, sorry i missed this yesterday. Congrats on being done with your thesis!

-Lucy

On Tue, Aug 18, 2015 at 2:27 PM, Stephen Noel <snoel07@vt.edu> wrote:

Lucy,

I would like to use your copyrighted figures from your May 28, 2014 thesis "Electronics for a Versatile and Robust Retarding Potential Analyzer for use in Nano-Satellite Platforms."

Figure 1: Overall block diagram for a satellite-borne RPA system.

Figure 3: Mechanical assembly of the CuRPA sensor surface. The printed circuit boards housing the control and measurement electronics are located beneath the collector.

Figure 4: RPA Electronic Functional Block Diagram

Figure 22: Vacuum chamber test set-up of the CuRPA.

As well as the unnamed figures (schematics) on pages 36, 37, 38, 39, and 40.

I have cited them appropriately in my thesis. Do I have your permission to use these figures in my thesis (attached)?

Best Regards,
Stephen Noel
LAICE VT Mission Manager
Virginia Tech
(540) 392-4872 [c]
stephen.noel@vt.edu

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