GPU-Based Acceleration for Interior Tomography

RUI LIU1,2, YAN LUO3, and HENGYONG YU1,2
1Department of Biomedical Engineering, Wake Forest University Health Sciences, Winston-Salem, NC 27157, USA
2Virginia Tech-Wake Forest University School of Biomedical Engineering and Sciences, Winston-Salem, NC 27157, USA
3Department of Electrical and Computer Engineering, University of Massachusetts, Lowell, MA 01854, USA
Corresponding author: H. Yu (hengyong-yu@ieee.org)

This work was supported in part by the National Science Foundation (NSF) CAREER under Award CBET-1149679 and in part by NSF Collaborative under Grant DMS-1210967.

ABSTRACT The compressive sensing (CS) theory shows that real signals can be exactly recovered from very few samplings. Inspired by the CS theory, the interior problem in computed tomography is proved uniquely solvable by minimizing the region-of-interest’s total variation if the imaging object is piecewise constant or polynomial. This is called CS-based interior tomography. However, the CS-based algorithms require high computational cost due to their iterative nature. In this paper, a graphics processing unit (GPU)-based parallel computing technique is applied to accelerate the CS-based interior reconstruction for practical application in both fan-beam and cone-beam geometries. Our results show that the CS-based interior tomography is able to reconstruct excellent volumetric images with GPU acceleration in a few minutes.

INDEX TERMS Computed tomography, compressed sensing, parallel computing, graphics processing unit, interior tomography.

I. INTRODUCTION

The x-ray computed tomography (CT) has been an indispensable imaging modality relying on multiple x-ray projections of the subject to reconstruct a two-dimensional (2D) or three-dimensional (3D) distribution of the attenuation coefficients within the subject [1]. Although it is proud of high spatial and temporal resolution [2], CT radiation accounts for a large portion of the ionizing radiation to the population that cannot be underestimated. The patients undergo a CT scan were estimated to be 60 million in 2002 in the United States, which occupied nearly 75% of the radiation exposure and almost 15% of the imaging procedures [3]. The widely accepted As Low As Reasonably Achievable (ALARA) principle urges the medical community to reduce the unnecessary radiation hazard as much as possible [4], [5]. Reducing the x-ray flux towards the detector and decreasing the amount of x-ray paths across the whole object supporting are two common strategies to avoid extra radiation. The former is usually achieved by controlling the operating current, voltage or exposure time but to leading high projection noise and the latter may produce few-view, limited-angle or truncated projection problems [6].

Conventional iterative reconstruction algorithms are immune to noisy projections in some extent. However, for the highly ill-posed reconstruction problem, additional regularization is necessary for unique and stable solutions. Interestingly, the compressive sensing (CS) theory shows that a real signal can be accurately recovered with an overwhelming probability from the amount of data or measurements far less than the Shannon-Nyquist sampling theorem claimed [7], [8]. The \( l_0 \) norm minimization is the basic paradigm of CS based signal recovering. Because of the NP-hard characteristic of the \( l_0 \) norm minimization, it is usually relaxed to the \( l_1 \) norm optimization with solid theoretical supports [9], [10]. Many algorithms have been proposed to solve the \( l_1 \) norm optimization problem, such as interior point method [11], gradient projection method [12] and some dedicated algorithms for the CS-based optimization [16]. In the medical imaging field, the total variation (TV, \( l_1 \) norm of discrete gradient transform (DGT) of an image) has been widely adopted as a regularization item. The conventional simultaneous algebraic reconstruction technique (SART) framework can be applied for CS-based image reconstruction by adding the TV regularization item [14]–[16]. The TV minimization is achievable either by using the steepest decent (SD) method or by using the soft-threshold filtering (STF) method. Many other TV minimization methods or applications were also reported, such as PICCS (prior image constrained compressed sensing) algorithm [17], CS based interior tomography in SIR (statistical iterative reconstruction) [18], and improved TV method in an ASD-POCS framework [19].

Narrowing down the x-ray beam to focus on a region-of-interest (ROI) is a representative method for dose reduction nominated as interior scan and the corresponding...
reconstruction is the interior problem. Because the interior problem is generally non-uniquely solvable, the interior scanning is unable to be applied for quantitative analysis based applications in clinics. However, limited by the high resolution detector size and the radiation dose reduction expectation, the interior scan is commonly desirable for many practical applications, such as cardiac CT [20] and Nano-CT [4]. Inspired by the CS theory, the interior problem has been proved uniquely and stably solvable by minimizing the ROI’s TV that the imaging object inside the ROI is piecewise constant [21], [22]. This knowledge regularized CT reconstruction algorithm is called CS-based interior tomography and the key is to minimize the TV of the ROI inside the imaging object.

The high computational cost (arithmetic operation and memory bandwidth) of the CS-based signal recovery algorithms hinders the sequential implementations of the iterative algorithms been applied in clinics especially for cone-beam spiral CT reconstruction [16]. Both the projection and backprojection processes are categorized as single instruction multiple data (SIMD) [23] computing model. The SIMD model is quite suitable for parallelization without too much complicated communication, synchronization or mutual lock mechanisms. Initially, parallel image reconstruction algorithms were implemented on clusters [24]. Cell processors were also applied in general purpose parallel computing [25]. Very recently, Intel released the Intel MIC (Intel Many Integrated Core) architecture products [26], and software engineers can run their codes on MIC with little or no additional workload. In early years, researchers began to accelerate their algorithms with graphics processing unit (GPU) when its programming interface was published for general purpose computing. Before GPU was programmable for general purpose computing, general algorithms were dedicatedly camouflaged as graphical operations such as texture mapping for parallel acceleration. Analytical reconstruction algorithms were benefited enormously from GPU [27]. Nowadays, two groups of programming interfaces for general computing in GPU have been developed. One is based on computer graphics languages, such as OpenGL [28], CG [29], HLSL [30], etc. The other is dedicated for high performance GPU computing, such as CUDA [23], OpenCL [31], Brook [32], etc. The CUDA (Compute Unified Device Architecture) [33]–[35] is rapidly exploited in many fields including medical imaging [35]. Comparing with other general GPU computing interfaces, the CUDA has higher performance and is easier to master and more flexible. Therefore, we choose CUDA to implement the CS-based interior reconstruction in GPU. For higher performance, the shared memory, texture memory and constant memory are applied for their high bandwidth and caching mechanism in our implementation.

In this paper, the SART and ordered-subset (OS)-SART reconstruction frameworks with TV minimization are implemented in GPU computing to make the CS-based interior tomography practical. In section II, the CT imaging model is reviewed with a concise SART-type reconstruction framework and the STF method. In section III, the implementation details are given. Section IV demonstrates the numerical results in 2D fan-beam and 3D cone-beam geometries. The SD and STF based TV minimization methods are compared in terms of reconstruction accuracy and speed. In section V, we discuss some related issues and conclude this paper.

II. ALGORITHM DESIGN

A. IMAGING MODEL

A 2D or 3D digital image can be expressed as \( f = (f_{i,j,k}) \in \mathbb{R}^N \), where \( N = I \times J \) for a 2D image and \( N = I \times J \times K \) for a 3D image. \( I, J \) and \( K \) are image pixel number in length, width and height dimensions. In this paper, both \( f_{i,j,k} \) and \( f_n \) are applied for convenience. Therefore, a CT system can be modeled as

\[
p = Af.
\]

Each component of the vector \( p \in \mathbb{R}^M \) is a measured datum, where \( M \) is the total measurements (the product of the projection number and the detector cell number), and \( A \in \mathbb{R}^{M \times N} \) is the system matrix. Typically, the \( n \)th pixel is viewed as a rectangular area with a constant value \( f_n \) and the \( m \)th projection datum \( p_m \) can be viewed as the summation of all the weighted pixel values involving the \( m \)th x-ray. Lots of discrete models have been proposed to calculate the entries of \( A \), such as linear interpolation method, grid method, distance-driven method [36], Siddons’ method [37], area integral method [38], footprint method [39], etc. To balance the speed and accuracy, the Siddons’ method is adopted as the projection model and the pixel-driven method is adopted as the backprojection model in our experiments. Siddons’ algorithm takes the length of the \( m \)th x-ray penetrating the \( n \)th rectangular pixel/voxel as the element \( a_{m,n} \) of the system matrix \( A \). An additive noise \( e \in \mathbb{R}^N \) is assumed, and the imaging process is finally modeled as

\[
p = Af + e.
\]

B. SART AND OS-SART

The SART-type solution for Eq. (2) is expressed as [40]

\[
f_n^{(l+1)} = f_n^{(l)} + \frac{\lambda(l)}{a_{l+n}} \sum_{m=1}^{M} a_{m,n} (p_m - A_m f_n^{(l)}),
\]

where \( a_{l+n} = \sum_{m=1}^{M} a_{m,n} > 0 \), \( a_{m,+} = \sum_{n=1}^{N} a_{m,n} > 0 \), \( A_m \) is the \( m \)th row of \( A \), \( l \) is the iteration index and \( 0 < \lambda(l) < 2 \) is a free relaxation parameter. For simplicity, let \( A_{++}^{N} \in \mathbb{R}^{N \times N} \) be a diagonal matrix with \( A_{++}^{N} = \frac{1}{a_{++}} \) and \( A_{M,+} \in \mathbb{R}^{M \times N} \) be a diagonal matrix with \( A_{M,+} = \frac{1}{a_{M,+}} \), Eq. (3) is rewritten as

\[
f^{(l+1)} = f^{(l)} + \lambda(l) A_{++}^{N} A^{T} A_{M,+} (p - Af^{(l)}).
\]

The ordered subset (OS) approach accelerates the convergence of SART by one to two orders of magnitude with the cost of inducing image bias [41], [42].
data is divided into \( T \) disjoint subsets \( B_t = \{ t_1, \ldots, t_B(t) \} \), where \( t = 1, 2, \ldots, T \), is the subset index of the projection. The union of these subsets covers the whole projection set. We have \( B_t \cap B_j = \emptyset (i \neq j, i, j \in \{1, 2, \ldots, T\} \) and \( \bigcup_{t=1}^{T} B_t = \{1, 2, \ldots, M\} \). These subsets alternatively participate the iterations. The OS-SART is represented as [35]

\[
f^{(l+1)}_n = f^{(l)}_n + \lambda^{(l)} \sum_{m \in B_t} \sum_{m' \in B_t} \frac{a_{m,n} (p_m - A_n f^{(l)}_n)}{(p_m - A_n f^{(l)}_n)},
\]

where \( t = (l \mod T) + 1 \). For every sub step in one iteration, it is suggested that the selected subset should be with the greatest possible angular distance from the previously used subset [41]. In our applications in this paper, the subset size is set as the detector cell number in one view. Meanwhile, the fast weighting technology in the FISTA (fast iterative shrinkage-thresholding algorithm) is applied to further accelerate the SART-type algorithms with a constant step size [43].

C. CS-BASED IMAGE RECONSTRUCTION

To further reduce the projections, the CS-based signal reconstruction method is combined with the OS-SART. The paradigm for CS-based signal recovering is a constrained \( l_0 \) norm minimization problem defined as

\[
\hat{x} = \arg_{x} \min \|x\|_0, \quad s.t. \ y = \Phi x,
\]

where \( x \) is a sparse signal, \( y \) is the observed data and \( \Phi \) is the sensing matrix. To address the NP-hard problem and suppress noise, Eq. (6) is usually modified as

\[
\hat{x} = \arg_{x} \min \|x\|_1, \quad s.t. \ \|y - \Phi x\|_2^2 \leq \varepsilon,
\]

where \( \varepsilon \) is the measurement error. For a sparser solution, the \( l_p \) norm minimization is also investigated. Generally speaking, the smaller the \( p \) is, the less measurements are needed for accurate reconstruction [44]. Actually, most of the signals in reality are far-fetched sparse, and the \( l_1 \) norm minimization paradigm is prohibited to be applied straightforwardly in medical image reconstruction. Usually, a sparse transform will be employed first to transform the non-sparse signal to an appropriate sparse domain. Assuming \( \Psi \) being the sparse transform, the CS-based CT image reconstruction paradigm can be finally expressed as

\[
\hat{f} = \arg_{f} \min \|\Psi f\|_1, \quad s.t. \ \|p - Af\|_2^2 \leq \varepsilon.
\]

D. DGT-BASED SPARSITY AND STF

The objective function \( \|\Psi f\|_1 \) in Eq. (8) can be defined as \( \|f\|_{TV} \) if \( \Psi \) is the discrete gradient transform (DGT) and the image object satisfy the piecewise constant assumption, where \( \|\| \|_{TV} \) denotes the \( l_1 \) norm of the DGT. The so-called Neumann condition [45] on the boundary is also assumed. With an appropriate Lagrange multiplier \( \sigma \), the problem Eq. (8) can be rewritten as:

\[
\hat{f} = \arg_{f} \min \|p - Af\|_2^2 + \sigma \|f\|_{TV}.
\]

Because the two items in Eq. (9) are convex, they can be alternatively minimized to yield an accurate solution.

While the OS-SART can be used to minimize \( \|p - Af\|_2^2 \), the conventional SD method can be employed to minimize the TV term. However, the SD based TV minimization sometimes over-enhances the edge regions and generates artifacts-like artifacts, and the step size needs to be carefully selected to find the minimum value and guarantee fast convergence. The STF (soft threshold filtering) is an alternative choice to minimize TV. Let \( Z = \{\xi_y\}_{y \in \Gamma} \) be a basis in \( \mathbb{R}^N \). \( f \) can be linearly expressed as \( f = \sum_{y \in \Gamma} (f \xi_y) \xi_y \). Let us define an objective function with positive weights \( \Omega = \{\omega_y\}_{y \in \Gamma} \)

\[
\mathcal{Z}_{\Omega,q}(f) = \|p - Af\|_2^2 + \sum_{y \in \Gamma} 2\omega_y |(f, \xi_y)|^q, \quad q \in [0, 2].
\]

When \( q = 1 \), finding the sparsest solution of \( \mathcal{Z}_{\Omega,1} \) is equivalent to Eq.(9) for DGT [14]. To find the sparse solution of \( \mathcal{Z}_{\Omega,1} \), we can recursively minimize \( \mathcal{Z}_{\Omega,1} \) in a STF framework

\[
\hat{f}^{(l+1)}_n = S_{\Omega,1}(\hat{f}^{(l)}_n + A^{T}(p - Af^{(l)}_n)),
\]

where \( \hat{f}^{(l)}_n \) is the intermediate image and

\[
S_{\Omega,1}(x) = \sum_{y \in \Gamma} S_{\omega_y,1}((x, \xi_y)) \xi_y
\]

is the functional that performs a soft threshold filtering. It has been proved by Daubechies et al in [15] that Eq.(11) is convergent. In Eq.(12), \( S_{\omega_y,1} \) is defined as

\[
S_{\omega_y,1}(x) = \begin{cases} 
 x - \omega_y, & \text{if } x \geq \omega_y \\
 0, & \text{if } |x| < \omega_y \\
 x + \omega_y, & \text{if } x \leq -\omega_y.
\end{cases}
\]

However, because the DGT is non-invertible and violates the restricted isometries property (RIP) [7], [46], the STF method is prohibited to be directly applied for TV minimization. This problem can be addressed by constructing a pseudo inverse of DGT [14], [16].

III. GPU ACCELERATION

A. PARALLELIZATION STRATEGY

For the CS-based interior tomography algorithm, the projection, backprojection, DGT, pseudo inverse transform, soft threshold filtering and finding the optimal threshold can be parallelized. Some constants (such as all the trigonometric functions) are pre-calculated and symbolically mapped to constant memory to achieve higher caching efficiency. However, this symbolical linking is optional because it costs almost the same clock cycles as fetching the data from the constant memory to calculate the coordinates corresponding to the current voxel or detector cells on the fly.

The on-board device memory usually is sufficient for fan-beam reconstruction unless storing the whole system matrix \( A \). Sometimes, it is also possible to compactly store \( A \) in the device memory when the image and the projection data...
are not too large. We compactly stored a 180,000 × 65,536 sparse matrix \( A \) in COO (Coordinate list, which stores a list of (row, column, value) tuples) format in CPU. The system matrix is transfer to the CSR (Compressed Sparse Row) format after it is loaded into the device memory. The projection process occupies 6.23ms with cuSPARSE (NVIDIA CUDA Sparse Matrix library). Actually, the whole program spent 2.26 seconds including reading matrix to the main memory and transferring to the device memory, etc. Even though a large device memory is provided, the storing of system matrix has to be gingerly designed to be fully compacted and the computational efficiency should be kept. Assuming that the image to be reconstructed is square, if the symmetry of 2D/3D scanning is fully utilized; only the first eighth of the scanning angles needs to be considered when the scanning range is \([0, 2\pi]\). The symmetrical relationship between image indices and sinogram indices are defined as follows:

\[
\begin{align*}
(i, j) & \rightarrow (\theta, t) \\
(R-j, i) & \rightarrow (\theta + \pi/2, t) \\
(R-i, R-j) & \rightarrow (\theta + \pi, t) \\
(j, R-i) & \rightarrow (\theta + 3\pi/2, t) \\
(R-j, R-i) & \rightarrow (\pi/2 - \theta, T - t) \\
(i, R-j) & \rightarrow (\pi - \theta, T - t) \\
(j, i) & \rightarrow (3\pi/2 - \theta, T - t) \\
(R-i, j) & \rightarrow (2\pi - \theta, T - t)
\end{align*}
\]

where \( i, j \) represents the pixel indices of the image, \( \theta \) is the current projection angle that can be easily transferred to the angle index, \( t \) is the index of the detector element and \( T \) is the detector resolution, and \( R \) is the image resolution in length or width direction. The pairs \((i, j)\) and \((\theta, t)\) satisfy the Radon transform relationship. In cone-beam geometry, not only the scanning range but also the projection symmetry on the upper and downer parts of the detector can be utilized if there is no offset. However, these symmetrical based tricks are not adopted here due to detector offset need to be considered in practical applications.

Typically, a sinogram is divided into adjoining sub-blocks in fan-beam case for parallel projection. Each sub-block corresponds to one thread block in CUDA and the threads in one block can be configured discretionally. The thread block is divided into \((p_x \times p_y)\) threads, where \( p_x \) addresses the detector indices and \( p_y \) addresses the projection angle indices with built-in variables in CUDA. On one hand, sufficient threads are required to accurately reconstruct the image object. On the other hand, it wastes computational resource if more threads are allocated than what are needed. It is optimal that the detector cell and the projection angle numbers are divisible by \( p_x \) and \( p_y \), respectively. The thread number being multiple of 32 in one block is sensible because the GPU executes 32 threads as a warp simultaneously. Fully occupying 1024 threads in one block is not optimal in Fermi architecture. The maximum warps for one multiprocessor in Fermi architecture is 48 that implies 1536 threads can be executed simultaneously. If the threads block is configured with fully 1024 threads, 1/3 of the computation resource will be idle. In the Kepler architecture, the maximum warp number increases to 64 in one multiprocessor. Therefore, 2048 threads can be executed simultaneously. Without considering the backward compatible for earlier GPUs, 1024 threads fill up one threads block in Kepler architecture and 512 threads are configured in Fermi architecture.

**B. PROJECT AND BACKPROJECTION MODELS**

Siddons’ algorithm is adopted as the projection model. An element \( a_{m,n} \) of system matrix \( A \) is computed as the length of \( m\text{th} \) x-ray passing through \( n\text{th} \) pixel/voxel for a 2D/3D image. In conventional CPU and GPU based implementations, additional memories are pre-allocated to store the parameters representing the intersections between an x-ray and the boundary box of each pixel/voxel. The pre-assigned memory should be at least \((N_x + N_y + N_z + 3) \times S_D \) bytes in cone-beam geometry, where \( N_x, N_y \) and \( N_z \) are the pixel numbers of the volume in length, width and height dimensions respectively, and \( S_D \) is the data size of each voxel. This may cause trouble for the CUDA programming with GK104 chip because it is banned to dynamically allocate device memory in the executing kernel. It is required to recompile the program if the image resolution changes and it also has to be implemented with macros to allocate the constant memory for storing the parameters. Although the constant expression feature in C++11 standard can solve this problem with the key word “constexpr”, this feature is not supported in Microsoft Visual Studio 2012 compiler. Therefore, a modified Siddons’ algorithm without pre-assigning memory was applied [47]. This algorithm first calculates the intersections of the compact support of the 2D/3D image and the current x-ray path, which is similar to the clipping algorithm in computer graphics [48]. Beginning with the incident point, the algorithm calculates the intersection points for each pixel/voxel for its weighting coefficient \( a_{ij} \). Iteratively, the exit point of the current pixel/voxel is set as the incident point of its neighbor pixel/voxel and this process will be repeated until the current incident point is the exit point located on the boundary of the object. After the current intersection point arrives at the exit point on the compact support, the innate ordered intersection parameters are all generated and the weighting for these intersection voxels can be calculated.

The pixel/voxel driven model is adopted to implement backprojection operation. For a given projection angle, mapping a point in the world coordinate to its corresponding detector index can be simplified to a geometrical transform matrix \( M \) in flat-panel detector case. This matrix can be constructed as follows. A homogeneous coordinate is assumed in our transform. The source and detector positions are both counterclockwisely rotated to make the source on the positive direction of Y axis with a rotation matrix \( R_s \), and the source is moved to the origin of the world coordinate by multiplying a matrix \( T_s \). After a perspective transform with \( P_s \), the transformed object point is projected to its shadow on the
detector. Finally, the shadow index on the detector can be easily calculated with two matrices \( T_m \) and \( S_u \). For concise, the offset of the detector, which can be easily integrated into the matrix multiplication in \( T_m \) is not considered in this derivation. Therefore, the matrices chain can be expressed as (15) shown at the bottom of the page, where \( u_0 \) and \( u_s \) are detector element size, \( m_x \) and \( m_z \) are minimum coordinate of the detector at the initial position, \( D \) is the source to detector distance, \( d \) is the source to iso-center distance and \( \theta \) is the current view angle. Because the ray-driven projection model mismatches the pixel/voxel driven backprojection process which means the system matrix in backprojection is not exactly the transpose of the projection system matrix. This mismatch will induce artifact with the increase of iteration number. Therefore, a method similar to Li et al. [49] is adopted to make the projection and backprojection match. The projection of a voxel on a plane can only be a convex polygon such as hexagon, pentagon or quadrilateral. The ray passing through the voxel must be inside its convex polygon shadow. First, for every voxel the boundary convex polygon is calculated. Then, the rectangle boundary box is calculated from the minimum and maximum projection coordinates of the voxel. Only the detector cells inside the boundary rectangular are considered as demonstrated in Figure 1. However, if the boundary rectangular is not large enough to contain multiple detector cells, it has no advantage over the simple pixel driven method but needs more computational complexity. When the detector resolution in pitch direction is the same as or smaller than the object resolution in the height dimension, some of the pixels in different slices will never be penetrated by any cone-beam x-ray in projection process. This will result in serious artifacts. To suppress this artifact, when the detector resolution is insufficient for high resolution reconstruction, we have to compromise the backprojection model to solve the model mismatching problem. Due to the specificity of this algorithm, a single thread is mapped to the vertex of a voxel instead of a single voxel. It is readily appreciated that 8 adjacent voxels share one vertex in three directions, and the redundant coordinate computing will occur if one thread response for one voxel updating. Therefore, the shared memory is applied to minimize latency caused by reading the same data from global memory multiple times and unnecessary computing. The thread block for backprojection is allocated as (8, 8, 8) and the thread index addresses current vertex. Because only the first 7 indices in each dimension response to the voxel updating, each thread block can update 343 voxels.

**C. PARALLELIZATION OF TV MINIMIZATION**

In 3D case, a general \( l_p \) norm of DGT is defined as

\[
L_p (D(f)) = \left( \sum_{k=1}^{K} \sum_{j=1}^{J} \sum_{i=1}^{I} |D(f_{i,j,k})|^p \right)^{\frac{1}{p}},
\]

where

\[
D(f_{i,j,k}) = \sqrt{(f_{i+1,j,k} - f_{i,j,k})^2 + (f_{i,j+1,k} - f_{i,j,k})^2 + (f_{i,j,k+1} - f_{i,j,k})^2},
\]

when \( p = 1 \), Eq. (16) degenerates to the TV. For an individual DGT value \( D(f_{i,j,k}) \) at the position \((i, j, k)\), it involves four values \( f_{i+1,j,k}, f_{i,j+1,k}, f_{i,j,k+1} \) and \( f_{i,j,k} \) which are sporadically stored in device global memory. Initially, this suggests each thread calculates one DGT value in position \((i, j, k)\) with a simple kernel function. Therefore, the threads configurations in fan-beam and cone-beam cases are the same as the configurations in the corresponding backprojection cases, respectively. In fact, the DGT is not intensive in arithmetic computation but in memory bandwidth. If all the data are stored in the global memory, every thread has to read 4 discontinuous data from the global memory and adjacent threads in one block have to read the same data which generate latency. Therefore, the volume is divided into overlapped sub-volumes with size \( 8 \times 8 \times 8 \). For one thread block, its indexed...
sub-volume is copied to the shared memory. In fan-beam case, similar strategies is applied, the image is divided into $32 \times 32$ overlapped sub-image, and for one thread block the corresponding sub-image is loaded into shared memory as shown in Figure 2. Because the value in last index of the sub image is the first index of its adjoining sub image, if the thread in each block is allocated as $(t_x, t_y)$ and the image size is $(N_x, N_y)$ in fan-beam case, the block number for DGT is $\left(\frac{N_x+t_x-2}{t_x-1}, \frac{N_y+t_y-2}{t_y-1}\right)$. Similarly, the block number in 3D case can be calculated using a similar formula.

Finding an optimal threshold after DGT is an indispensable step to accelerate the convergence of the STF, which can be achieved by dichotomy in GPU. The Thrust library is applied for this implementation [48]. We first copied the discrete gradient image $\hat{d}^{(l)}$ to $\hat{d}^{(l)}$ and then $\hat{d}^{(l)}$ is ascending sorted. The prior knowledge of the intermediate discrete gradient image can be estimated from the roughly reconstructed image by the classical FBP approach. The soft-threshold filtering is applied with respect to each component according to Eq. (13).

The pseudo inverse transform will be applied to inversely transform the image from the DGT to image domain, which is implemented in GPU according to Eqs. (3.8) to (3.11) in [14].

The mechanism bandwidth problem can also be solved by the shared memory similar to the DGT implementation. Different from the DGT transform, the inverse threshold filtering needs more shared memory. In 2D case, the thread block is also configured with size (32, 32). Considering that the transform in position $(i, j)$ relates to all its adjacency pixels, only $30 \times 30$ pixels in the center of the sub image can be calculated by the current thread block as in Figure 2. Therefore the block number should be $\left(\frac{N_x+t_x-3}{t_x-2}, \frac{N_y+t_y-3}{t_y-2}\right)$ if the image resolution is $N_x \times N_y$ and the threads configuration in one block is $(t_x, t_y)$. The block number for 3D pseudo inverse transform can be calculated in a similar fashion.

D. OVERALL PSEUDO-CODES

Combining the OS-SART and STF, the CS-based interior tomography can be implemented as the following pseudocodes:

S0: Initializing $l = 0$, $\hat{f}^{(l)} = 0$ and estimating $\omega_{\gamma_0}$;
S1: Updating $l \leftarrow l + 1$ and performing OS-SART to update $\hat{f}^{(l)}$;
S2: Performing DGT from $\hat{f}^{(l)}$ to $\hat{d}^{(l)}$ corresponding to $(\hat{f}, \zeta_\nu)$ in Eq.(13);
S3: Performing soft-threshold filtering from $\hat{d}^{(l)}$ to $\hat{d}^{(l)}$ using Eq. (14);
S4: Performing pseudo inverse DGT to obtain $\hat{f}^{(l)}$; [14]
S5: Updating $\hat{f}^{(l)} \leftarrow \hat{f}^{(l)}$;
S6: If the stopping criteria are met, output the result; otherwise go to S1.

IV. NUMERICAL EXPERIMENTS

A. PLATFORM CONFIGURATION AND GEOMETRY PARAMETERS

The SART and OS-SART algorithms are implemented in GPU for both fan-beam and cone-beam geometries with CUDA/C++ language. For the CS-based interior tomography, the TV regularization is applied with the aforementioned SD and STF methods. All the experiments are tested on a high-performance workstation configured as follows. Two Intel Xeon CPUs are configured with core clock frequency 3.10GHz. Each CPU contains 16 cores. The memory size is 32GBs. The operating system is Microsoft Windows 7.

The memory bandwidth problem can also be solved by the shared memory similar to the DGT implementation. Different from the DGT transform, the inverse threshold filtering needs more shared memory. In 2D case, the thread block is also configured with size (32, 32). Considering that the transform in position $(i, j)$ relates to all its adjacency pixels, only $30 \times 30$ pixels in the center of the sub image can be calculated by the current thread block as in Figure 2. Therefore the block number should be $\left(\frac{N_x+t_x-3}{t_x-2}, \frac{N_y+t_y-3}{t_y-2}\right)$ if the image resolution is $N_x \times N_y$ and the threads configuration in one block is $(t_x, t_y)$. The block number for 3D pseudo inverse transform can be calculated in a similar fashion.

D. OVERALL PSEUDO-CODES

Combining the OS-SART and STF, the CS-based interior tomography can be implemented as the following pseudocodes:

S0: Initializing $l = 0$, $\hat{f}^{(l)} = 0$ and estimating $\omega_{\gamma_0}$;
S1: Updating $l \leftarrow l + 1$ and performing OS-SART to update $\hat{f}^{(l)}$;
S2: Performing DGT from $\hat{f}^{(l)}$ to $\hat{d}^{(l)}$ corresponding to $(\hat{f}, \zeta_\nu)$ in Eq.(13);
S3: Performing soft-threshold filtering from $\hat{d}^{(l)}$ to $\hat{d}^{(l)}$ using Eq. (14);
S4: Performing pseudo inverse DGT to obtain $\hat{f}^{(l)}$; [14]
S5: Updating $\hat{f}^{(l)} \leftarrow \hat{f}^{(l)}$;
S6: If the stopping criteria are met, output the result; otherwise go to S1.

IV. NUMERICAL EXPERIMENTS

A. PLATFORM CONFIGURATION AND GEOMETRY PARAMETERS

The SART and OS-SART algorithms are implemented in GPU for both fan-beam and cone-beam geometries with CUDA/C++ language. For the CS-based interior tomography, the TV regularization is applied with the aforementioned SD and STF methods. All the experiments are tested on a high-performance workstation configured as follows. Two Intel Xeon CPUs are configured with core clock frequency 3.10GHz. Each CPU contains 16 cores. The memory size is 32GBs. The operating system is Microsoft Windows 7.

The memory bandwidth problem can also be solved by the shared memory similar to the DGT implementation. Different from the DGT transform, the inverse threshold filtering needs more shared memory. In 2D case, the thread block is also configured with size (32, 32). Considering that the transform in position $(i, j)$ relates to all its adjacency pixels, only $30 \times 30$ pixels in the center of the sub image can be calculated by the current thread block as in Figure 2. Therefore the block number should be $\left(\frac{N_x+t_x-3}{t_x-2}, \frac{N_y+t_y-3}{t_y-2}\right)$ if the image resolution is $N_x \times N_y$ and the threads configuration in one block is $(t_x, t_y)$. The block number for 3D pseudo inverse transform can be calculated in a similar fashion.

D. OVERALL PSEUDO-CODES

Combining the OS-SART and STF, the CS-based interior tomography can be implemented as the following pseudocodes:

S0: Initializing $l = 0$, $\hat{f}^{(l)} = 0$ and estimating $\omega_{\gamma_0}$;
S1: Updating $l \leftarrow l + 1$ and performing OS-SART to update $\hat{f}^{(l)}$;
S2: Performing DGT from $\hat{f}^{(l)}$ to $\hat{d}^{(l)}$ corresponding to $(\hat{f}, \zeta_\nu)$ in Eq.(13);
S3: Performing soft-threshold filtering from $\hat{d}^{(l)}$ to $\hat{d}^{(l)}$ using Eq. (14);
S4: Performing pseudo inverse DGT to obtain $\hat{f}^{(l)}$; [14]
S5: Updating $\hat{f}^{(l)} \leftarrow \hat{f}^{(l)}$;
S6: If the stopping criteria are met, output the result; otherwise go to S1.
projection view. Meanwhile, the GPU-computing is suitable for CS-based interior reconstruction especially when only a few projections are available. With the improvement of the image resolution, the speedup is more obvious. This is because when the image is small, the projection and back-projection generally are more bandwidth intensive instead of computing intensive.

The SD-based TV minimization in the OS-SART framework is also tested to compare with the STF-based algorithm with image resolution $1024^2$ and $2048^2$ in fan-beam geometry. The bar chart in Figure 3 shows the speedup results. The reconstruction time between the OS-SART and the other two TV regularization based algorithms are minimal, and the STF-based TV minimization algorithm runs a little bit faster than the SD-based TV minimization.

A single-floating-precision $512^2$ modified 3D Shepp-Logan phantom is applied to test the speedup performance in cone-beam geometry with the OS-SART algorithm plus TV regularization. A detector resolution of $600^2$ and 100 views are applied. The computational cost for the STF-based and the SD-based TV minimization in the OS-SART framework are respectively 1665.46 and 1791.33 seconds after 85 iterations. For one iteration step, the computational costs in different algorithms are listed in Table 3. The sum of projection, backprojection and regularization time is not exactly the same as the total time because other time is needed for data loading from disk, data transfer, functions calling and so on. The computational cost is relatively small to find the optimal threshold $\omega_0$ in the OS-SART with STF-based TV minimization. With more detailed analysis, the GPU utilization for the projection and backprojection steps are both 100.0%, the utilization to find the optimal threshold $\omega_0$ including filtering is 100.0%, while the occupation rate for DGT is only 75%.

### TABLE 1. Cone-beam reconstruction geometry configuration for numerical simulations and clinical dataset.

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Numerical Simulations</th>
<th>Clinical Dataset</th>
</tr>
</thead>
<tbody>
<tr>
<td>Source to detector distance</td>
<td>1000mm</td>
<td>947mm</td>
</tr>
<tr>
<td>Source to iso distance</td>
<td>850mm</td>
<td>539mm</td>
</tr>
<tr>
<td>Detector Type</td>
<td>Equidistant</td>
<td>Equiangular</td>
</tr>
<tr>
<td>Scan range</td>
<td>0 to 360 degrees</td>
<td>-267 to 93 degrees</td>
</tr>
<tr>
<td>View number</td>
<td>CATCE</td>
<td>2200</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Detector Parameters</th>
<th>Size</th>
<th>Arc</th>
<th>Height</th>
</tr>
</thead>
<tbody>
<tr>
<td>Entries per row</td>
<td>CATCE</td>
<td>888</td>
<td>65.5296mm</td>
</tr>
<tr>
<td>Truncation ratio</td>
<td>50%</td>
<td>43.7%</td>
<td></td>
</tr>
<tr>
<td>Row Number</td>
<td>CATCE</td>
<td>64</td>
<td></td>
</tr>
<tr>
<td>Offset</td>
<td>NONE</td>
<td></td>
<td>0.767925mm</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Image Parameters</th>
<th>Size</th>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>Resolution</td>
<td>CATCE</td>
<td>512$^2\times64$</td>
<td>0.625mm</td>
</tr>
<tr>
<td>Slices interval</td>
<td>CATCE</td>
<td>2200</td>
<td></td>
</tr>
</tbody>
</table>

### TABLE 2. The computational cost for STF-based TV minimization in an OS-SART framework with a modified Shepp-Logan phantom. A comparison is presented between CPU (Intel Xeon 3.1GHz, single core usage) and GPU (NVIDIA, Tesla K10, single GPU usage) after 50 iterations.

<table>
<thead>
<tr>
<th>Image size</th>
<th>Detector size</th>
<th>View number</th>
<th>Time (s)</th>
<th>Speedup Factors</th>
<th>Time (s)</th>
<th>Speedup Factors</th>
<th>Time (s)</th>
<th>Speedup Factors</th>
</tr>
</thead>
<tbody>
<tr>
<td>$256^2$</td>
<td>300</td>
<td>17</td>
<td>CPU 19.8</td>
<td>1.1</td>
<td>GPU 18</td>
<td></td>
<td>CPU 33.8</td>
<td>9.3</td>
</tr>
<tr>
<td>$512^2$</td>
<td>600</td>
<td></td>
<td>CPU 70.9</td>
<td>2.5</td>
<td>GPU 28</td>
<td></td>
<td>CPU 149.4</td>
<td>9.3</td>
</tr>
<tr>
<td>$1024^2$</td>
<td>1200</td>
<td></td>
<td>CPU 257.7</td>
<td>7.0</td>
<td>GPU 37</td>
<td></td>
<td>CPU 556.3</td>
<td>26.8</td>
</tr>
<tr>
<td>$2048^2$</td>
<td>2400</td>
<td></td>
<td>CPU 1066.4</td>
<td>23.4</td>
<td>GPU 46</td>
<td></td>
<td>CPU 2368.4</td>
<td>90.2</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Image size</th>
<th>Detector size</th>
<th>View number</th>
<th>Time (s)</th>
<th>Speedup Factors</th>
<th>Time (s)</th>
<th>Speedup Factors</th>
<th>Time (s)</th>
<th>Speedup Factors</th>
</tr>
</thead>
<tbody>
<tr>
<td>$256^2$</td>
<td>300</td>
<td>180</td>
<td>CPU 63.5</td>
<td>8.7</td>
<td>GPU 7</td>
<td></td>
<td>CPU 120.9</td>
<td>17.1</td>
</tr>
<tr>
<td>$512^2$</td>
<td>600</td>
<td></td>
<td>CPU 320.8</td>
<td>22.7</td>
<td>GPU 14</td>
<td></td>
<td>CPU 633.2</td>
<td>45.1</td>
</tr>
<tr>
<td>$1024^2$</td>
<td>1200</td>
<td></td>
<td>CPU 1176.5</td>
<td>65.6</td>
<td>GPU 18</td>
<td></td>
<td>CPU 2396.8</td>
<td>130.6</td>
</tr>
<tr>
<td>$2048^2$</td>
<td>2400</td>
<td></td>
<td>CPU 4769.9</td>
<td>220.8</td>
<td>GPU 22</td>
<td></td>
<td>CPU 9083.9</td>
<td>439.1</td>
</tr>
</tbody>
</table>
A single-floating-precision $512^2 \times 256$ image volume is also reconstructed with the OS-SART. The projection geometry is in Table 1 except that the detector resolution changes to $1024 \times 512$ and the views increases to 360. An iteration number 10 is applied for this study. The total reconstruction time is 423.20 seconds. In one iteration, 42.3 seconds are needed to project the $512^2 \times 256$ image volume to $1024 \times 512 \times 360$ projections and to backproject the projections. From the performance comparison with different reconstruction scales in image resolution, detector resolution and views, we can see that the performance of boundary box based backprojection is largely influenced by the detector resolution. To further accelerate the reconstruction, the dataset is divided into two smaller ones and distributed to dual GPUs evenly. Except the same geometry configuration, both the volumetric image and the raw projections are halved. An iteration number 30 is assumed. There is no data redundancy to guarantee an accurate reconstruction since the symmetry of circular cone-beam scanning geometry. While the computational cost is 583.46 seconds for single Kepler GK104, the computational cost is reduced to 289.85 seconds with two Kepler GK104s. The GUPS (Giga-updates per second) is also tested for the
R. Liu et al.: GPU-Based Acceleration for Interior Tomography

FIGURE 4. Image volume reconstructed from a patient data set by the OS-SART algorithm. (a)-(c) are the transverse, sagittal and coronal planes, respectively. The display window is $[-1000\text{HU}, 2500\text{HU}]$. (d)-(f) are the corresponding difference images of (a)-(c) related to the ones reconstructed by the FDK method in a narrow display window. (g)-(i) are three pseudo-color volumetric rendering in different observation positions, which are generated by the NVIDIA CUDA SDK under the license EULA (the NVIDIA end user license agreement).

backprojection step. On average, it takes 7.39 seconds for the backprojection step to update $512^3$ image from 360 views in once iteration. This implies that for each view it spends 20.52ms and the GUPS is 6.09.

The recently released unified memory technique in CUDA 6.0 creates a pool of managed memory that is shared between CPU and GPU. It bridges the CPU-GPU gap. This technique makes the programming convenient without considering the data transfer among devices. At the same time, it simplifies the huge volumetric reconstruction when the GPU device memory is insufficient. As the documentation suggested, the raw projection data and the volume to be reconstructed are both declared with key word “__managed__”. We implemented a miniature of the projection and backprojection with unified memory technique. The performance from one view with the same geometry configuration makes us a little frustrating. Projecting a $512^2 \times 256$ volume to $1024 \times 512$ detector takes 214.312ms (22.5% of the time) and backprojecting the same data costs 736.69ms (77.5% of the time). It needs more complicated implementation details to accelerate the performance which will be an extension of our work.

Under the approval of the institutional review board of Wake Forest University Health Sciences, a clinical patient dataset is also reconstructed with the configuration in Table 1. To fully utilize all the GPU resources in our device, three GK104 chips including two Tesla K10s and one GeForce GTX 670 are all occupied. This configuration can be approximately viewed as cone-parallel geometry because the divergence angle is small enough to be ignored and the projection data can be evenly distributed to three GPUs without data redundancy. When only SART is used to reconstruct the image volume, the total computational cost for 160 iterations is 1268.46 seconds. This means 7.93 seconds are needed for one iteration on average. On the other hand, if only one core is applied in CPU implementation, 884.07 seconds are required for one iteration. Therefore, GPU implementation can accelerates the reconstruction more than 110 times in this case. The ordered subset technique is also applied to accelerate the convergence. The projection is divided into 50 subsets and each subset contains 44 views. Totally, 536.12 seconds are required for 40 iterations which guarantee a promising result as shown in Figure 4, and 13.40 seconds are needed for one iteration. This is caused by the following reason: when the subset number increases, although it can accelerate the convergence with more projection and backprojection processes to traverse all the projection views, it will slow down the execution in one loop. The interior scan is simulated by

VOLUME 2, 2014 765
truncating 43.7% of the clinical dataset as in Table 1. Because only the interior part is illuminated due to the projection truncation, the FOV is reduced to $230^2 \text{mm}^2$. The projection is divided into 22 subsets for the OS-SART. The computational cost is 161.203 seconds in total for 20 iterations. This means that $\sim 2.5$ minutes can give promising interior reconstruction results as shown in Figure 5.

### C. IMAGE QUALITY

First, the image quality of interior reconstruction is evaluated with a modified Shepp-Logan phantom in fan-beam geometry. The iteration number is 30. Some representative interior reconstruction results are shown in Figure 6. The images reconstructed by the OS-SART from few view projections show serious artifacts inside the internal ROI. The SD-based and STF-based TV minimizations have similar performance after decades of iterations. The slight differences between SD-based and STF-based TV minimizations can be seen from the reconstructions of highly sparse views.

Another group of reconstruction comparison is based on the clinical patient dataset. The sinogram for the central slice in Figure 5 is extracted and uniformly downsampling from 2200 to 180 views. The iterations number is also 30. The interior parts of the reconstruction results are shown in Figure 7. The RMSE (root-mean-square deviation) is calculated for the ROI indicated by the red circle in Figure 7(a) to quantitatively evaluate the image quality. While the RMSE of Figure 7(b) is 88.28HU, the RMSE of figure 7(c) is 79.81HU. The SSIM (the structural similarity index) is calculated to evaluate the similarity [52]. The SSIM for the STF-based OS-SART is 0.9140 while the SSIM for the SD-based OS-SART is 0.8723. The two quantitative measurements show that the STF-based OS-SART outperforms the SD-based OS-SART in this study.

In cone-beam geometry, a $512^3$ single floating precision modified Shepp-Logan phantom is reconstructed from 72 views. The detector resolution is $600^2$ with the simulation configuration summarized in Table 1. The reconstructed results in transverse, sagittal and coronal planes are shown in Figure 8. The SSIM on these three planes are 0.9587, 0.9850 and 0.9690, respectively in the STF-based reconstruction method. In the SD-based reconstruction method, the corresponding SSIM values are 0.9279, 0.9226 and 0.9029.
FIGURE 7. Reconstructed results of a cardiac region from clinical projections. (a) is the reference image, (b) is reconstructed by the SD-based TV minimization, and (c) is reconstructed by the STF-based TV minimization. For (a) to (c), the display window is $[-1000\text{HU}, 1800\text{HU}]$. (d) and (e) are the corresponding difference images of (b) and (c) related to (a), respectively, and the display window is $[-2400\text{HU}, 400\text{HU}]$.

For the clinical patient dataset, it can be easily observed from Figure 4 that the differences are really small between the images reconstructed by the OS-SART and FBP algorithm. Moreover, as shown in Figure 7, the STF-based TV minimization provides better reconstruction result compared to the SD-based TV minimization for interior tomography. To validate the convergence of the STF-based OS-SART algorithm, 50 views are uniformly sampled from the sinogram of the central slice to reconstruct an image of $256^2$. Figure 9 shows the reference and seven intermediate images with respect to different iteration numbers. From images (b) to (h), the image quality improves gradually with the increase of the iteration number. However,
FIGURE 9. Interior reconstruction results from 50 projections after different iterations. The image size is 256 × 256. (a) is the reference image reconstructed by the FBP method from 2200 global projections. (b) to (h) are the reconstructed images from 50 projections after 10, 20, 100, 200, 500, 1000 and 5000 iterations, respectively.

FIGURE 10. Reconstruction error curve with respect to iterative number. The errors are computed in reference to region marked by the red circle in image (a) in Fig. 9. Two subfigures inside the main figure are the local amplifications of the main curve.

when the iterations number is sufficient large (e.g. 200), the image quality becomes stable. From the convergence curve in Figure 10, one can see that the reconstruction error (the sum of pixel error squares) decreases rapidly in the first decades of iterations, reaches the minimizer after 130 iterations, then increases a little bit and finally becomes stable after 200 iterations.

V. CONCLUSION
The x-ray CT is one of the most important imaging modalities for non-destructive diagnosis and image-guided intervention despite the potential radiation risks. To reduce the radiation dose, we have implemented the CS-based interior reconstruction in GPU for fan-beam and cone-beam reconstruction in this paper. The TV regularization is adopted in our work by incorporating the SD or STF methods. These two methods are both implemented and compared. To test the reconstruction performance, we performed several groups of experiments with different reconstruction parameters from simulated and real datasets in both fan-beam and cone-beam geometries.

The GPU parallel computing can be used to boost the CS-based interior tomography for practical applications. We implemented the CS-based interior tomography in GPU devices for fast reconstruction. Our experimental results show that the OS-SART with STF-based TV minimization method runs slightly faster than the SD-based TV minimization and reconstruct promising results in fan-beam geometry using one GPU for acceleration. In the cone-beam geom-
etry experiments, the STF-based method outperforms the SD-based method for few-view projections. Comparing with the CPU-based implementation in fan-beam geometry, the speedup is higher when the views are smaller or the image resolutions are larger in cone-beam case, and the reconstruction speedup with real data is obvious. Therefore, the GPU parallelization is suitable for CS-based interior tomography especially for large-scale volumetric reconstruction. By analyzing the timeline in cone-beam reconstruction, it is found that the projection and backprojection operations dominate the reconstruction cost in the STF-based method. In the near future, we will optimize the implementation and investigate other projection and backprojection models for possible high-efficient GPU implementation to further reduce the computational cost. Other regularization methods will also be studied with implementations in GPU to further decrease the number of views for interior reconstruction. The unified memory technique will be studies to deal with the very large volumetric reconstruction cases.

It is not surprising that the STF-based OS-SART outperforms the SD-based OS-SART. The SD-based TV minimization requires tentatively choosing the parameters such as the descent steps and the descent iteration numbers. It is more likely to find the minimum solution with small steps while the convergence is inferior and the descent iteration number influences the reconstruction speed. On the other hand, the deficiency of STF is its convergence rate. To accelerate the convergence, it is necessary to choose the optimized threshold which is related to dichotomy. The searches range from several times to dozens of times. Actually, there are no too much visual differences between the results reconstructed by the SD-based OS-SART and the STF-based OS-SART. The RMSE and SSIM show that the reconstruction results are comparable with more iterations. For a clinical volumetric reconstruction cases.

REFERENCES


RUI LIU is currently pursuing the Ph.D. degree in biomedical engineering with the Department of Biomedical Engineering, Wake Forest University Health Sciences, Winston-Salem, NC, USA. He received the bachelor’s degree in computer science and technology from South China Normal University, Guangzhou, China, in 2009, and the master’s degree in computer architecture from the Institute of Computing Technology, Chinese Academy of Sciences, Beijing, China, in 2012. His research interests include computer tomography and medical image processing with an emphasis on GPU acceleration for compressive sensing based interior reconstruction algorithms.

YAN LUO (M’05) is an Associate Professor with the Department of Electrical and Computer Engineering, University of Massachusetts Lowell, Lowell, MA, USA. He received the Ph.D. degree in computer science from the University of California at Riverside, Riverside, CA, USA, in 2005, and the B.E. and M.E. degrees from the Huzhong University of Science and Technology, Wuhan, China, in 1996 and 2000, respectively. While his research interest spans broadly computer architecture and network systems, his current research focuses on heterogeneous architecture and systems, software-defined networks, and deep learning. He has served on the program committee of numerous international conferences, and has served as a Guest Editor and referee of premier journals. He directs the Laboratory of Computer Architecture and Network Systems, which has been supported by the National Science Foundation, Intel, Raytheon/BBN, Xilinx, and Altera. He has published numerous peer-reviewed journal and conference papers with one Best Paper Award. He is a member of the Association for Computing Machinery.

HENGYONG YU (SM’06) is an Assistant Professor and the Director of the CT Laboratory with the Department of Biomedical Engineering, Wake Forest University Health Sciences, Winston-Salem, NC, USA. He received the bachelor’s degrees in information science and technology, and computational mathematics, and the Ph.D. degree in information and communication engineering from Xi’an Jiaotong University, Xi’an, China, in 1998, 1998, and 2003, respectively. He was an Instructor and Associate Professor with the College of Communication Engineering, Hangzhou Dianzi University, Hangzhou, China, from 2004 to 2010. His interests include computed tomography and medical image processing. He has authored and co-authored more than 100 peer-reviewed journal papers with an H-index of 21 according to the Web of Knowledge. He is the founding Editor-in-Chief of JSM Biomedical Imaging Data Papers, serves as an Editorial Board member of Signal Processing, Journal of Medical Engineering, CT Theory and Applications, International Journal of Biomedical Engineering and Consumer Health Informatics, and Open Medical Imaging Journal, and the Guest Editor of the IEEE TRANSACTIONS ON MEDICAL IMAGING, the IEEE ACCESS, and International Journal of Biomedical Imaging. He is a Senior Member of the IEEE Engineering in Medicine and Biology Society, and a member of the American Association of Physicists in Medicine and the Biomedical Engineering Society. He was for a recipient of the Outstanding Doctoral Dissertation Award from Xi’an Jiaotong University in 2005, and the best Natural Science Paper Award from the Association of Science and Technology of Zhejiang Province, and the NSF CAREER Award for development of CS-based interior tomography in 2012.