Reference

A. General Power Factor Correction Techniques

- [A1] W. F. Ray and R. M. Davis, "The definition and importance of power factor for power electronics converters," Proc. European conference on Power Electronics and Applications (EPE), 1988, pp. 799-805
- [A2] M. Grotzbach, "Line side behavior of uncontrolled rectifier bridges with capacitive DC smoothing," Proc. European Conference on Power Electronics and Applications (EPE), 1989, pp.761-764
- [A3] P. Mehta, M. Darwish and T. Thomson, "A novel technique for reactive power compensation," Proc. European Conference on Power Electronics and Applications (EPE), 1989, pp. 1169-1174
- [A4] Electromagnetic Compatibility (EMC) Part 3: Limits Section 2: Limits for harmonic current emissions (equipment input current < 16 A per phase), IEC 1000 3 2 Document, First Edition, 1995
- [A5] K. H. Liu and Y. L. Lin, "Current waveform distortion in power factor correction circuits employing discontinuous mode boost converter," IEEE Power Electronics Specialists Conference, 1989, pp. 825-829
- [A6] C. Silva, "Power factor correction with the UC3854," Application Notes, Unitrode Integrated Circuits.
- [A7] L. H. Dixon, Jr., "High power factor pre-regulator for off-line power supplies," Unitrode Switching Regulator Power supply Design Seminar Manual, Paper I2, SEM-700, 1990.
- [A8] E. X. Yang, G. C. Hua, Y. Jiang, and Fred C. Lee, "Isolated boost circuit for power factor correction," IEEE Applied power Electronics conference, 1993, pp. 196-203
- [A9] C. Zhou and M. M. Jovanovic, "Design Trade-off in continuous current mode controlled boost power factor correction circuits," Virginia Power Electronics Seminar, 1992, pp. 57-68
- [A10] Y. Jiang and Fred C. Lee, "A new control scheme for Buck + Boost power factor correction circuit," Virginia Power Electronics Seminar, 1993, pp.189-194

- [A11] D. Maksimovic and R. Erickson, "Universal input, High Power factor boost doubler rectifiers," IEEE Applied Power Electronics Conference, 1995, pp.459-465
- [A12] R.Erickson, M.Madigan and S.Singer, "Design of a simple high power factor rectifier based on the flyback converter," IEEE Applied Power Electronics Conference, 1990, pp.792-801
- [A13] W. Tang, Y. Jiang, G. C. Hua, Fred C. Lee and I Cohen, "Power factor correction with flyback converter employing charge control," IEEE Applied Power Electronics Conference, 1993, pp.293-298
- [A14] R. Watson, G.C. Hua and Fred C. Lee, "Characterization of an active clamp flyback topology for power factor correction applications," IEEE Applied Power Electronics Conference, 1994, pp. 412 – 418
- [A15] M.K. Nablant, "Power factor calculations and measurements," IEEE Applied Power Electronics Conference (APEC) Proc., Mar. 1990, pp. 543-552
- [A16] J. B. Klaassens and M. P. N. Wesenbeeck, "Series resonant single phase AC to DC power supply with active power factor control," European Conference on power electronics and applications (EPE), 1989, pp.689-694
- [A17] J. He and N. Mohan, "Input current shaping inline rectification by resonant converters," IEEE Industry Applications Society Annual Meeting, 1987, pp.990-995

B. Integrated Single-Stage Power Factor Correction Techniques

- [B1] M. Madigan, R. Erickson and Ismail, "Integrated high quality rectifier-regulators," IEEE Power Electronics Specialists Conference, 1992, pp. 1043-1051.
- [B3] D. M. Tsang, Fred C. Lee and M.M. Jovanovic, "Improvements in the integrated high quality rectifier-regulators," Virginia Power Electronics Seminar, 1993, pp.115-123
- [B3] R. Redl, L. Balogh, "Design consideration for single stage isolated power factor corrected power supplies with fast regulation of the output voltage," IEEE Applied Power Electronics Conference, 1995, pp.454-458
- [B4] R. Redl, L. Balogh and nathan O.sokal, "A new family of single stage isolated power factor correctors with fast regulation of the output voltage," IEEE Power Electronics Specialists Conference, 1994, pp. 1137-1144

- [B5] Jinrong Qian, "Advanced single-stage power factor correction techniques," Ph.D. Dissertation, Virginia Power Electronics Center, Sep. 1997
- [B6] G. C. Hua, "A novel CCM single-stage power factor correction converter," U.S. Patent NO. 5,790,389, Aug. 4, 1998.
- [B7] J. Sebastian, M. M. Hernando, P. Villegas, J. Diaz and A. Fontam, "Input current shaper based on the series connection of a voltage source and a loss-free resistor," IEEE Applied Power Electronics Conf. (APEC) Proc. 1998, pp. 461-467
- [B8] J. Sebastian, M. M. Hernando, P. Villegas, J. Diaz and A. Fontam, "A new input current shaping technique using converters operating in continuous conduction mode," IEEE Power electronics Specialists Conference, 1998, pp. 1330-1336
- [B9] Laszlo Huber and Milan M. Jovanovic, "Singe-stage, single-switch, isolated power supply technique with input-current shaping and fast output-voltage regulation for universal line input-voltage-range application," IEEE Applied Power Electronics Conf(APEC) Proc. 1997, pp.272-280.
- [B10] F. Tsai, P. Markowski, and E. Whitcomb, "Off-line flyback converter with input harmonic correction," IEEE International Telecommunications Energy Conference, 1996, pp. 120-124
- [B11] J. Qian, F. C. Lee, "A high efficiency single-stage single-switch high power factor AC/DC converter with universal line input," IEEE Applied Power Electronics Conference, 1997, pp. 281-287
- [B12] H. Watanabe, Y. Kobayashi and Y. Sekine, "The suppressing harmonic currents, MS (Magnetic-Switch) power supply," IEEE International Telecommunications Energy Conference, 1995, pp. 281-287
- [B13] S. Teramoto, M. Sekine, R. Saito, "High power factor AC/DC converter," U.S. Patent No. 5,301,095, Apr. 5, 1994
- [B14] I. Takahashi and R. Y. Igarashi, "A switching power supply of 99% power factor by dither rectifier," International Telecommunication Energy Conference Proceedings, 1991, pp. 714 - 719
- [B15] Jinrong Qian, Qun Zhao and Fred C. Lee, "Single-stage single-switch power factor correction AC/DC converters with DC bus voltage feedback for universal line applications," IEEE Applied Power Electronics Conf. (APEC) Proc. 1998. pp.223-229

- [B16] M. M. Jovanovic, D. M. C. Tsang and F. C. Lee, "Reduction of voltage stress in integrated high-quality rectifiers-regulators by variable-frequency control," IEEE Applied Power Electronics Conference (APEC) Proc., 1994, pp. 569-575
- [B17] R. N. Do Prado, S. A. Bonaldo, M. C. Moreira, D. L. Vidor, "Electronic ballast with a high power factor for fluorescent lamps," IEEE Power Electronics Specialists Conference, 1996, pp. 1215-1219
- [B18] J. Qian, Fred C. Lee, "Investigation of charge pump controller high power factor AC/DC converter," Virginia Power Electronics Seminar, 1996, pp.15-21
- [B19] Y. Jiang, Fred. C. Lee, "Single-stage single-phase parallel power factor correction scheme," IEEE Applied Power Electronics Conf. (APEC), 1994, pp.1145-1151

C. Design and comparison study of single-stage PFC converters

- [C1] L. Huber and M. M. Jovanovic, "Design optimization of single-stage, single-switch input-current shaper," IEEE Power electronics Specialists Conference, 1997, pp.519-526
- [C2] B. Sharifipour, J. S. Huang, P. Liao, L. Huber and M. M. Jovanovic, "Manufacturing and cost analysis of power-factor-correction circuits," IEEE Applied Power Electronics Conf. (APEC) Proc., 1997, pp.490-494