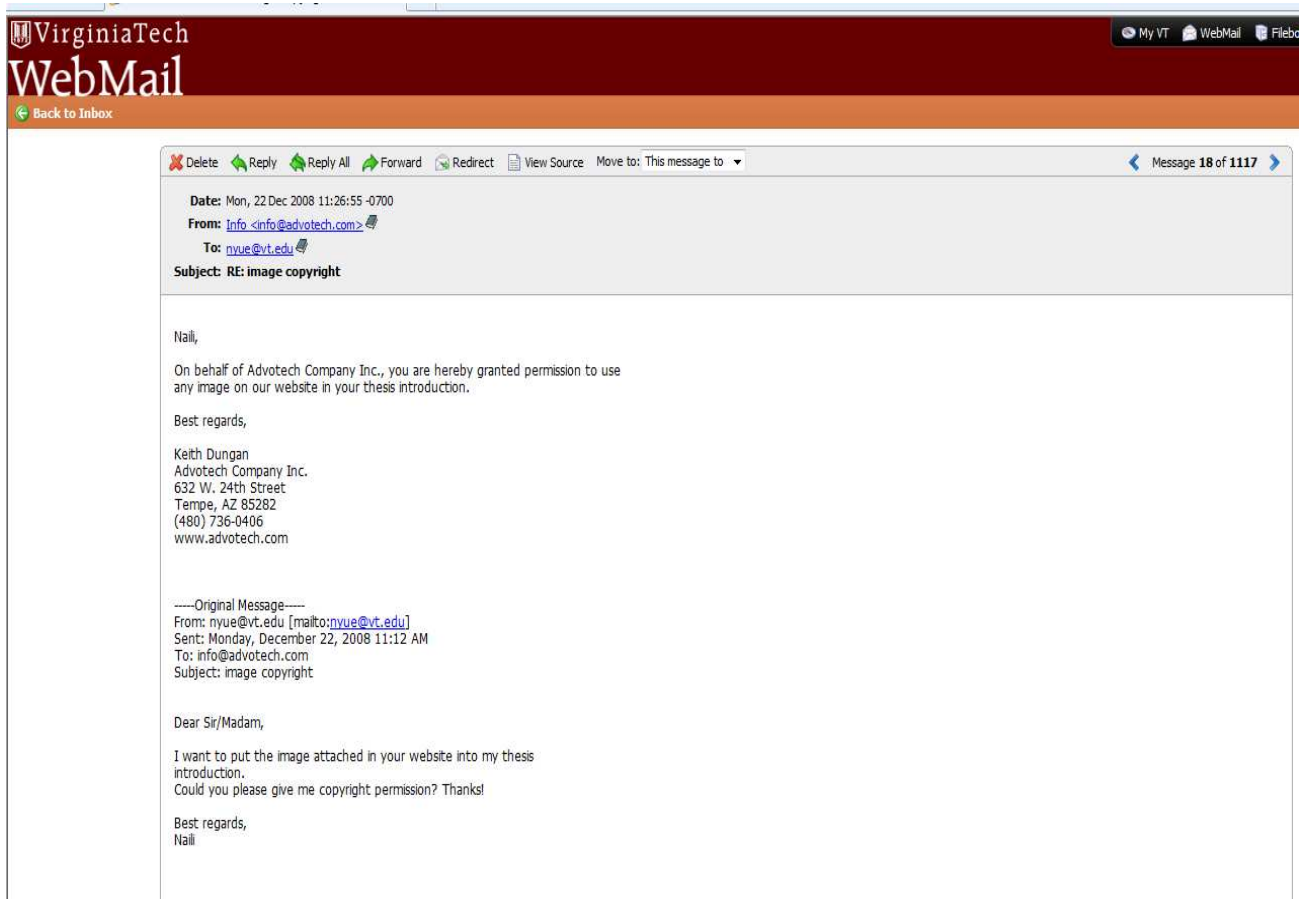


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1. Figure 1.2

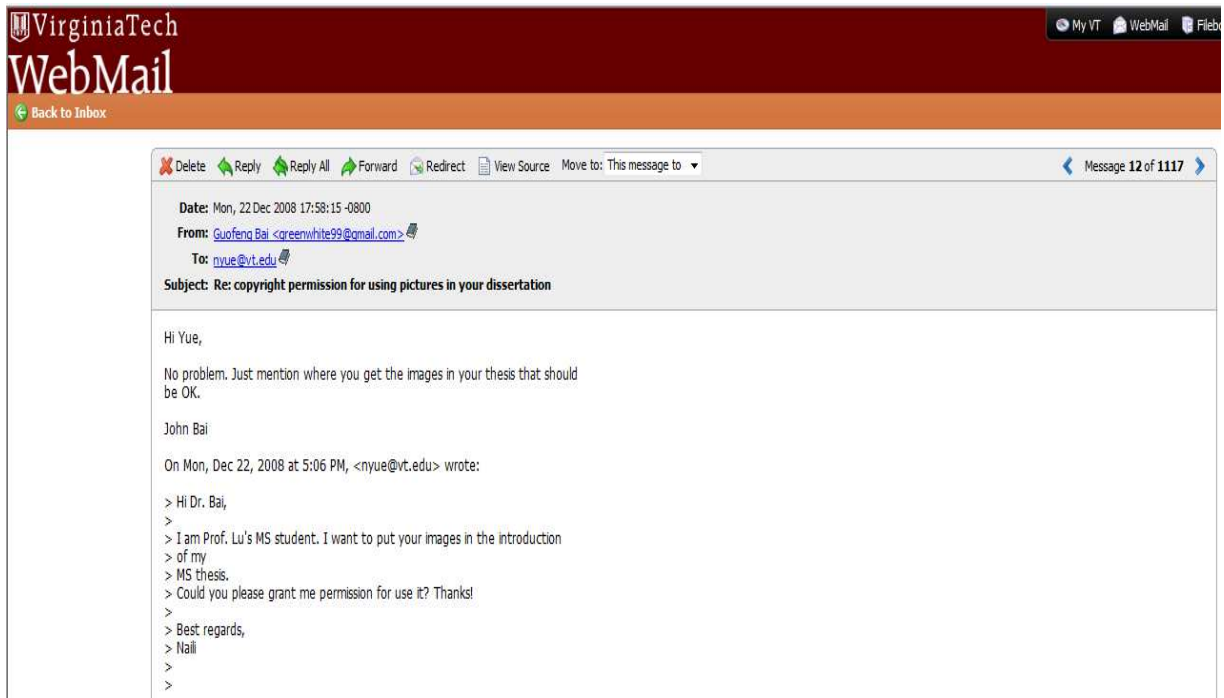
**ADVOTECH COMPANY INC.
632 W. 24TH STREET
TEMPE, AZ 85282**

**PHONE: 480-736-0406
FAX: 480-736-0206
info@advotech.com**



2. Figure 1.6, Figure 1.7, Figure 1.9, Figure 1.21, and Figure 1.24

J. G. Bai, “Low-temperature sintering of nanoscale silver paste for semiconductor device interconnection,” *Ph.D Dissertation*, Virginia Polytechnic Institute and State University, Blacksburg, VA 2005.



3. The rest figures or images are from author