

# Power Fingerprinting for Integrity Assessment of Embedded Systems

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Dissertation submitted to the Faculty of the  
Virginia Polytechnic Institute and State University  
in partial fulfillment of the requirements for the degree of

Doctor of Philosophy  
in  
Electrical Engineering

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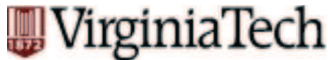
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