

# Techniques for Seed Computation and Testability Enhancement for Logic Built-In Self Test

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in partial fulfillment of the requirements for the degree of

Master of Science  
in  
Computer Engineering

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Theories (SMT), Integer Linear Programming (ILP), Test-point Insertion

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Source: VLSI Test Principles and Architectures, Morgan Kaufmann, 2006, p.46

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<http://www.itrs.net/Links/2008ITRS/Home2008.htm> , International Technology Roadmap for Semiconductors, 2009.

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